



T55x7/T5555

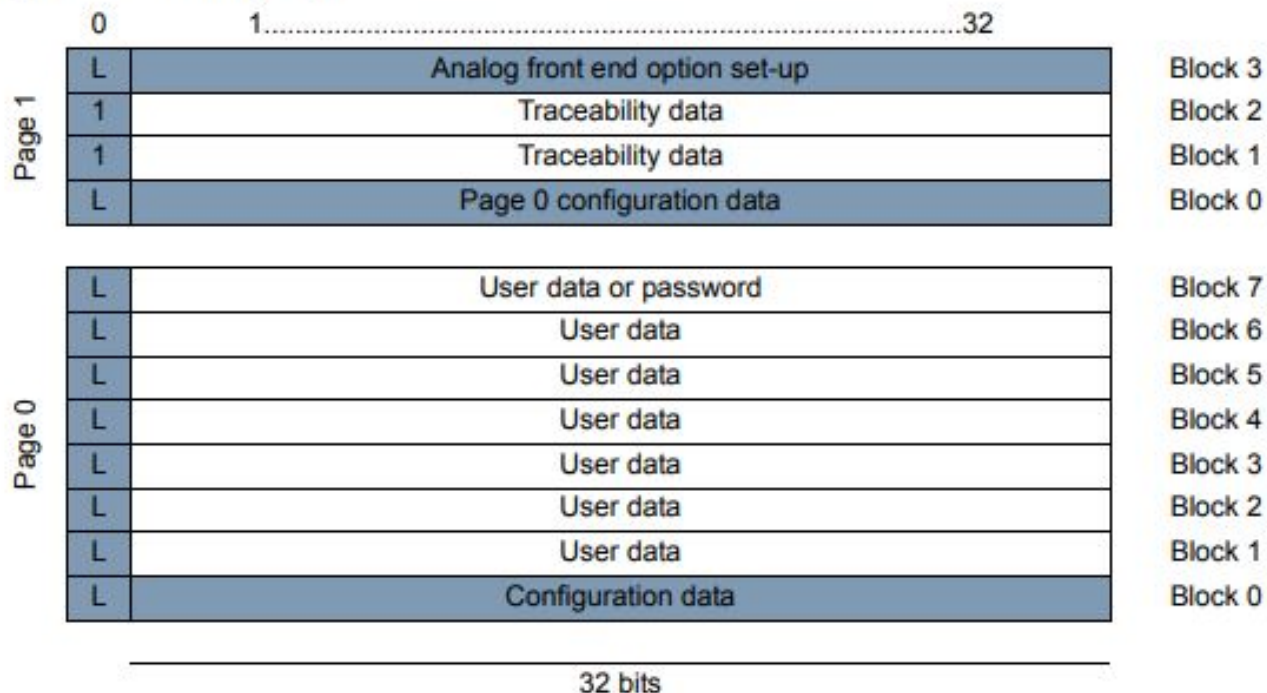


Informacje ogólne

- 125kHz
- ISO/IEC 11784/785

Mapa pamięci

Figure 4-2. Memory Map



Traceability data

Figure 4-3. Atmel ATA5577C Traceability Data Structure

Example:	"E0h" 8 bit		"15h" 8 bit		"0000 1b" 5 bit			"010b" 3 bit		"9h" 4 bit		"00b" 2 bit		"00b" 2 bit						
Bit No.	1	...	8	9	...	16	17	...	21	22	...	24	25	...	28	29	30	31	32	
Block 1	ACL			MFC			CID			ICR		Year		Quarter		Number				
Bit value	63																32			
																MSB				
Bit value	31																0			
																LSB				
Block 2	Number					Wafer#			DW											
Bit No.	1	...	12	13	...	17	18	...	31	32										
	12 bit					5 bit			15 bit											
Example:	"0000 1010 0100b"					"0110 0b"			"000 0100 1101 0010b"											

lf t55xx dump

```
Reading Page 0:
blk | hex data | binary | ascii
-----+-----+-----+-----
00 | 000880E0 | 00000000000010001000000011100000 | ....
01 | 00000000 | 00000000000000000000000000000000 | ....
02 | 00000000 | 00000000000000000000000000000000 | ....
03 | 00000000 | 00000000000000000000000000000000 | ....
04 | 00000000 | 00000000000000000000000000000000 | ....
05 | 00000000 | 00000000000000000000000000000000 | ....
06 | 00000000 | 00000000000000000000000000000000 | ....
07 | 00000000 | 00000000000000000000000000000000 | ....
Reading Page 1:
blk | hex data | binary | ascii
-----+-----+-----+-----
00 | 000880E0 | 00000000000010001000000011100000 | ....
01 | 00000000 | 00000000000000000000000000000000 | ....
02 | 00000000 | 00000000000000000000000000000000 | ....
03 | 00000000 | 00000000000000000000000000000000 | ....
```

lf t55xx trace

```
[=] Chip type..... T55x7
[=] Modulation..... ASK
[=] Bit rate..... 2 - RF/32
[=] Inverted..... No
[=] Offset..... 33
[=] Seq. terminator... Yes
[=] Block0..... 000880E0 (auto detect)
[=] Downlink mode..... default/fixed bit length
[=] Password set..... No
```



Koniec